

#### ST763A series

## 3.3V Step down current mode PWM DC-DC converters

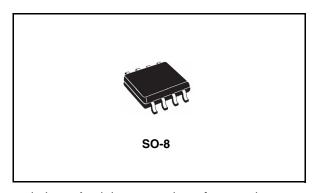
#### **Features**

- Output voltage 3.3V
- Supply voltage range from 3.3V to 11V
- Guaranteed output current: 500mA
- Typical operation frequency: 200KHz
- Very low quiescent current: 0.6mA on mode 0.2µA OFF MODE
- Switch ON/OFF control
- Typical efficiency: 90%
- Operating temperature range: -40°C to 85°C
- Available in SO-8 package



The ST763A is a step-down switching regulator. It operates from 3.3V to 11V giving a fixed 3.3V output voltage, delivering up to 500mA. The mainly features are typical efficiency of 90%, quiescent current of 0.6mA, and only 0.2µA in shut-down.

The PWM current mode control provides precise output regulation and very good transient response. Output voltage accuracy is guaranteed to be  $\pm 5\%$  over line, load and temperature



variations. A minimum number of external components is used and the fixed frequency switching allows easy filtering of output ripple and noise.

Other features of this device are cycle-by-cycle current limiting, overcurrent limiting, under voltage lockout and programmable soft-start protection.

A  $22\mu H$  inductor works in most applications, so no sophisticated design is necessary.

Package available is SO-8.

Typical application are in 5V to 3.3V converters, cellular phones, portable instruments, hand-held computers, and peripherals.

#### Order code

Part number	Packaging
ST763AC	ST763ACDTR

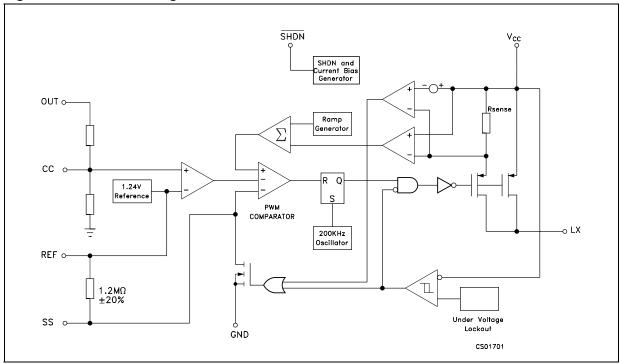
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2	Pin configuration
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4	Typical application
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ST763A series Diagram

# 1 Diagram

Figure 1. Schematic diagram



Pin configuration ST763A series

# 2 Pin configuration

Figure 2. Pin connections (top view)

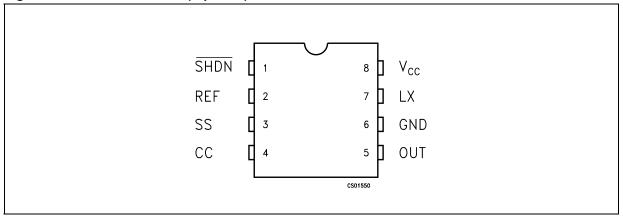


Table 1. Pin description

Pin N°	Symbol	Name and function
1	SHDN	Shutdown control (active low): If connected to GND the IC is in shutdown. Connect to $V_{CC}$ for normal operation (ON MODE).
2	REF	Reference output voltage: (1.25V): Bypass to GND with 47nF capacitor.
3	SS	Soft start: a capacitor between SS and GND provides soft-start and short-circuit protections.
4	CC	Compensation capacitor input: externally compensates the outer (voltage) feedback loop. Connect to OUT with 330pF capacitor
5	OUT	Output voltage sense input: provides regulation of feedback sensing. Connect to 3.3V output.
6	GND	Ground
7	LX	Switch output. Drain of internal P-channel power MOSFET
8	V <sub>CC</sub>	Supply voltage input. Bypass to GND with $1\mu F$ ceramic capacitance and large value electrolytic capacitor in parallel. The $1\mu F$ capacitor must be as close as possible to the GND and $V_{CC}$ pins.

ST763A series Maximum ratings

# 3 Maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit	
V <sub>CC</sub>	DC input voltage		-0.3 to 12	V
$V_{LX}$	Switch pin voltage		-0.3 to (V <sub>CC</sub> + 0.3)	V
V <sub>SHDN</sub>	Shutdown voltage (SHDN)		-0.3 to (V <sub>CC</sub> + 0.3)	V
$V_S,V_C$	Soft start (SS) and compensation capacitor (C	-0.3 to (V <sub>CC</sub> + 0.3)	V	
I <sub>LX</sub>	Switching peak current	2	Α	
I <sub>REF</sub>	Reference current	2.5	mA	
P <sub>TOT</sub>	Continuous power dissipation at T <sub>A</sub> = 70°C SO-8		344	mW
T <sub>stg</sub>	Storage temperature range	-40 to +150	°C	
T <sub>op</sub>	Operating junction temperature range	0 to +70	°C	

Note:

Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied

Table 3. Thermal data

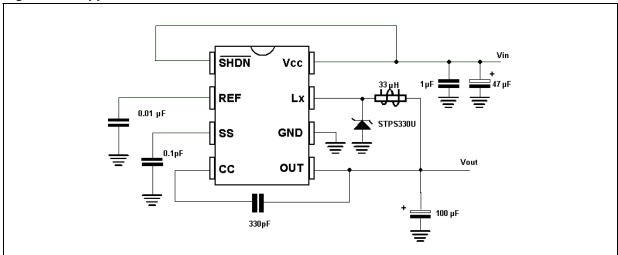
Symbol	Parameter	SO-8	Unit
R <sub>thJA</sub>	Thermal resistance junction-ambient (1)	160	°C/W

<sup>1.</sup> This value depends from thermal design of PCB on which the device is mounted.

Typical application ST763A series

# 4 Typical application

Figure 3. Application circuit



## 5 Electrical characteristics

Table 4. Electrical characteristics

(V<sub>CC</sub> = 5V, I<sub>O</sub> = 0mA,  $T_A = T_{MIN}$  to  $T_{MAX}$ , unless otherwise specified).

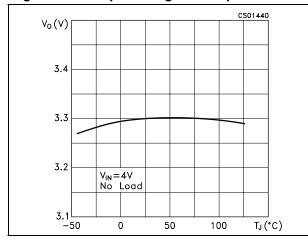
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V <sub>CC</sub>	Input voltage		3.3		11	V
V	Output voltage	$V_{CC} = 4 \text{ to } 11V, I_{O} = 0 \text{ to } 300\text{mA}$	3.135	3.3	3.465	V
Vo	Output voltage	$V_{CC} = 4.75 \text{ to } 11 \text{V}, I_{O} = 0 \text{ to } 500 \text{mA}$	3.135	3.3	3.465	V
ΔV <sub>O</sub>	Line regulation			0.13		%/V
ΔV <sub>O</sub>	Load regulation	I <sub>O</sub> = 0 to 500mA		0.005		%/mA
n	Power efficiency	I <sub>O</sub> = 300mA		88		%
η	Power efficiency	I <sub>O</sub> = 100mA		90		70
	Cupply ourrent	ON Mode (Including switch current)		0.6	2.5	mA
I <sub>SUPPLY</sub>	Supply current	OFF Mode		0.6 2.5   0.2 100		
V <sub>IH</sub>	SHDN Input high threshold		2			٧
V <sub>IL</sub>	SHDN Input low threshold				0.25	٧
I <sub>SHDN</sub>	Shutdown input leakage current				1	μΑ
V <sub>LOCK</sub>	Under voltage lockout	V <sub>CC</sub> Falling		2.7	3	٧
R <sub>DS(on)</sub>	LX On resistance	I <sub>LX</sub> = 500mA		1		Ω
I <sub>LEAK</sub>	LX Leakage current	$V_{CC} = 12V$ , $V_{LX} = 0V$		10		nA
V <sub>REF</sub>	Reference voltage	T <sub>A</sub> = 25°C	1.18	1.25	1.32	٧
$\Delta V_{REF}$	Temperature reference drift	$T_A = T_{MIN}$ to $T_{MAX}$		50		ppm/°C
fosc	Switching frequency	T <sub>A</sub> = 25°C	159	200	212.5	KHz
R <sub>C</sub>	Compensation pin impedance			7500		Ω

## **6** Typical performance characteristics

(Unless otherwise specified  $T_J = 25^{\circ}C$ )

Figure 4. Output voltage vs temperature

Figure 5. Output voltage vs input voltage



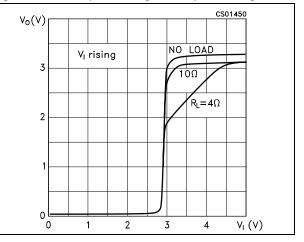
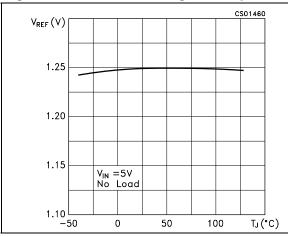


Figure 6. Reference voltage vs temperature

Figure 7. Efficiency vs temperature



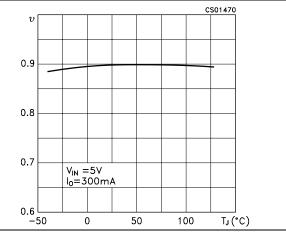
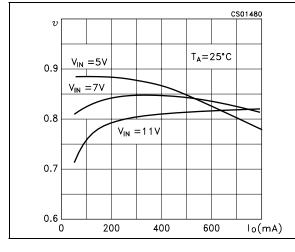
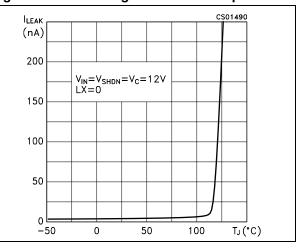


Figure 8. Efficiency vs output current

Figure 9. LX leakage current vs temperature





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CS01500 CS01510 Rosor  $V_{\text{SHDN}}$ (V)  $(\Omega)$ 1.5 1.5 V<sub>IN</sub>=12V 1.0 1.0  $V_{IN}=5V$ 0.5 0.5 V<sub>IN</sub>=5V I<sub>0</sub>=500mA  $R_{\text{LOAD}}\!=\!330\Omega$ 0 └ -50 0 └ -50 0 100 T<sub>J</sub> (°C) 0 T<sub>J</sub> (°C) 100

Figure 10. LX ON Resistance vs temperature Figure 11. Shutdown input threshold vs temperature

Figure 12. Shutdown input leakage current vs Figure 13. Oscillator frequency vs temperature temperature

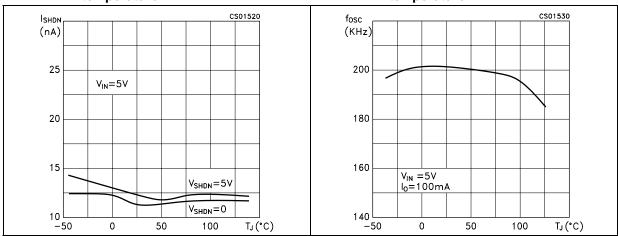
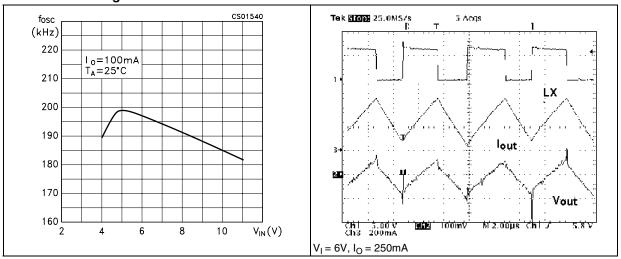


Figure 14. Oscillator frequency vs input voltage

Figure 15. Switching waveform, continuous conduction



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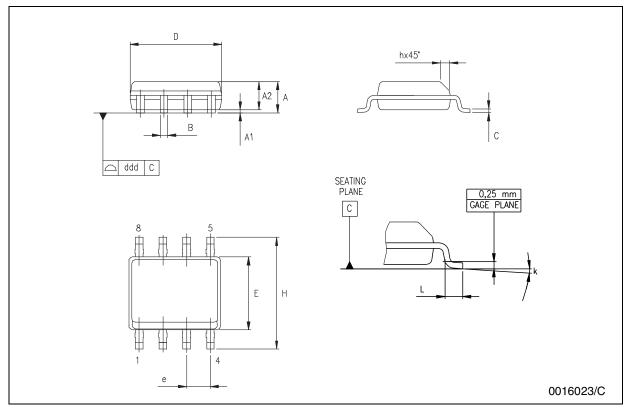
## 7 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK<sup>®</sup> packages. These packages have a Lead-free second level interconnect. The category of second Level Interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.



#### **SO-8 MECHANICAL DATA**

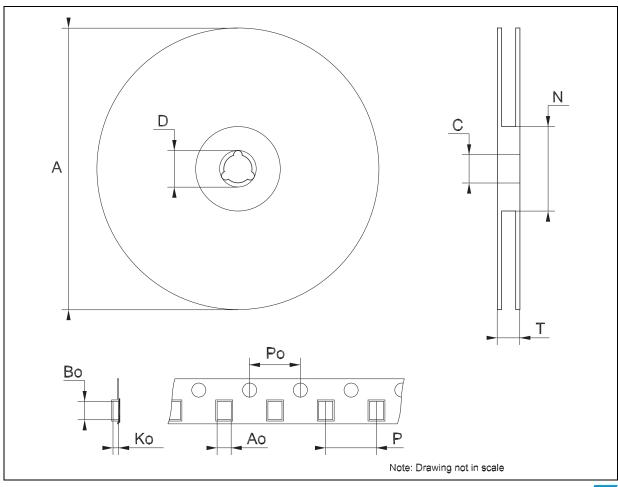
DIM.		mm.			inch	
DIN.	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
А	1.35		1.75	0.053		0.069
A1	0.10		0.25	0.04		0.010
A2	1.10		1.65	0.043		0.065
В	0.33		0.51	0.013		0.020
С	0.19		0.25	0.007		0.010
D	4.80		5.00	0.189		0.197
E	3.80		4.00	0.150		0.157
е		1.27			0.050	
Н	5.80		6.20	0.228		0.244
h	0.25		0.50	0.010		0.020
L	0.40		1.27	0.016		0.050
k			8° (n	nax.)		•
ddd			0.1			0.04



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Tape & R	eel SO-8	<b>MECHANICAL</b>	_ DATA
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DIM.		mm.			inch	
DIWI.	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
Α			330			12.992
С	12.8		13.2	0.504		0.519
D	20.2			0.795		
N	60			2.362		
Т			22.4			0.882
Ao	8.1		8.5	0.319		0.335
Во	5.5		5.9	0.216		0.232
Ko	2.1		2.3	0.082		0.090
Po	3.9		4.1	0.153		0.161
Р	7.9		8.1	0.311		0.319



ST763A series Revision history

# 8 Revision history

Table 5. Revision history

Date	Revision	Changes
26-May-2006	2	Add T <sub>A</sub> row f <sub>OSC</sub> on table 4 and new template.
29-May-2007	3	Order code has been updated.

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